THEREFOR

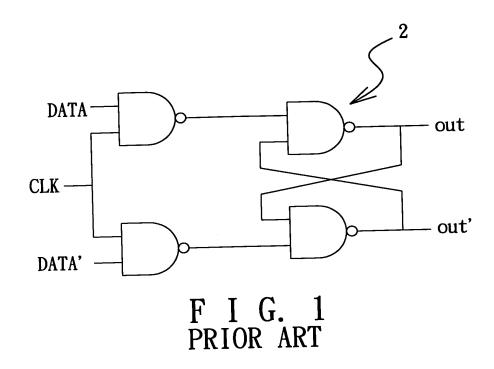
Inventors:

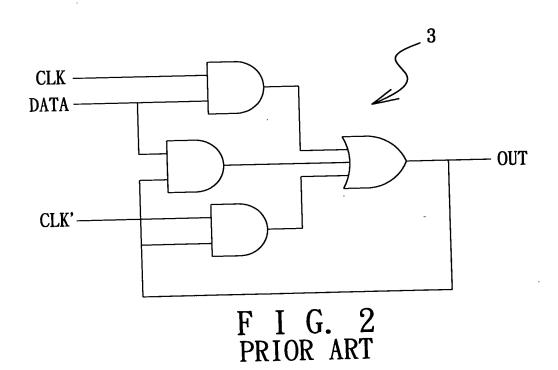
Tsin-Yuan Chang; Hao-Yung Lo;

and Shao-Sheng Yang

Docket No.:

SIPT121661

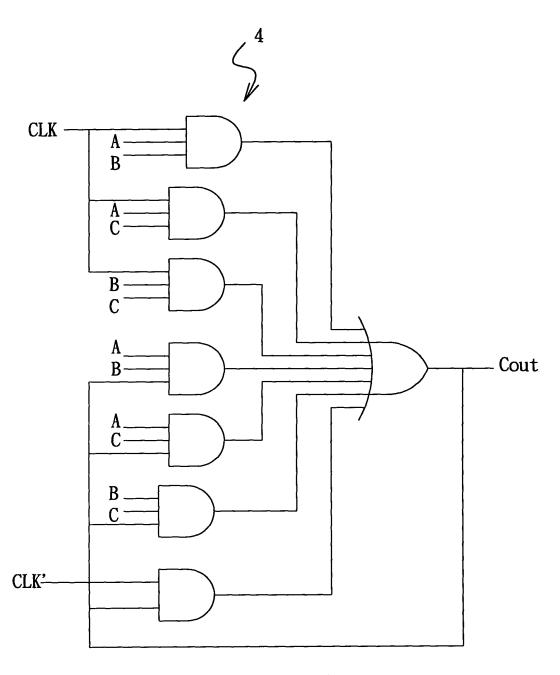




THEREFOR

Tsin-Yuan Chang; Hao-Yung Lo; Inventors:

and Shao-Sheng Yang
Docket No.: SIPT121661



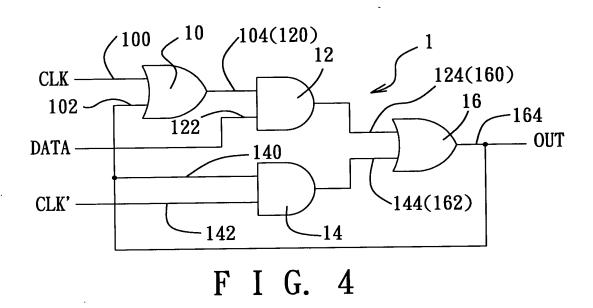
F I G. 3 PRIOR ART

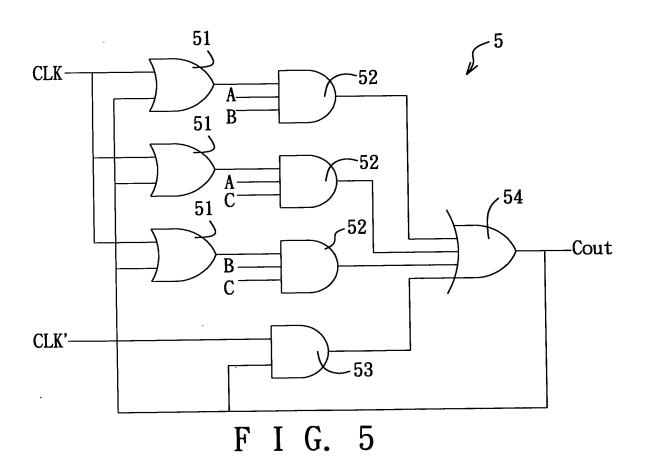
THEREFOR

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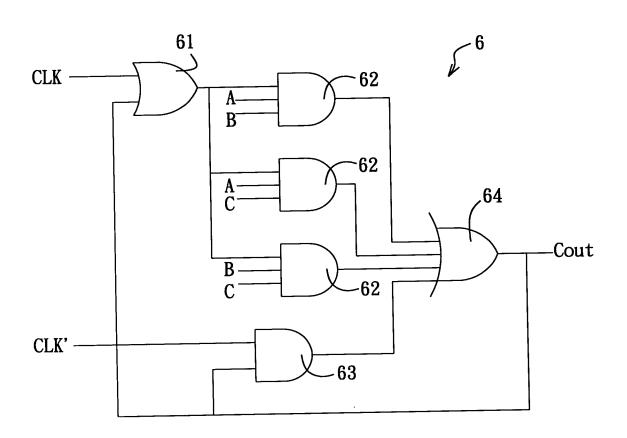




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Docket No.: SIPT121661



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Tsin-Yuan Chang; Hao-Yung Lo; Inventors: and Shao-Sheng Yang
Docket No.: SIPT121661 ರಾ F I G. 7 ∞ [__ H 140n 140n 140n 120n 120n 120n 100n 100n 100n In 80n Time (lin) (TIME) n 80n Time (lin) (TIME) Time(lin)(TIME) cout_original 40n ê cout_proposed, besodord_mus 20n 20n 20n N (nil) segetloV VOL VOL VOL VOL Voltages (lin) ٥ Δ **D** DO: AO: v(clk) DO: A0: v(co) DO: AO: v(cp) DO: AO: v(so) DO: AO: v(sp) DO: AO: v(c) 00:A0:v(b)

Mave

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Title: FEEDBACK LATCH CIRCUIT AND METHOD THEREFOR

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THEREFOR

Inventors:

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ed			6	<u>&</u>
Modified Svensson latch		39	0.209	0.148
Tri-state Modified buffer Svensson latch	10	48	0.396	0.299
transmission buffer -gate latch	10	48	0. 255	0.363
Jamb latch	9	36	0.262	0.328
Conventional Conventional Latch Latch Latch	18	54	0.313	0.164
Conventional D-latch	18	72	0.284	0.316
Present	14	36	0.274	0.144
	Unit area	Total	Delay (ns)	Power (mW)